



DDR2 SDRAM REGISTERED DIMM MODULE, 1.8V

512MByte - 64MX72

AVF7264R52E5667F2

FEATURES

JEDEC DDR2 PC2-5300 667MHz

- Clock frequency: 333MHz with CAS latency 5
- 256 byte serial EEPROM
- Data input and output masking
- Programmable burst length: 2, 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 5
- Auto refresh and self refresh capability
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- SSTL-2 compatible inputs and outputs
- Decoupling capacitors at each memory device
- Single-sided module
- 1.18 inch height

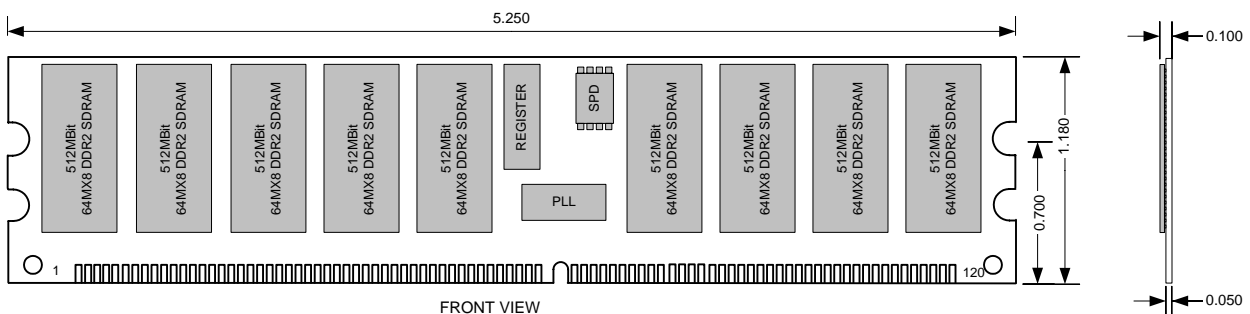
DESCRIPTION

The AVF7264R52E5667F2 is a Registered DDR2 SDRAM DIMM module. This module is JEDEC Pinout compatible DDR2 SDRAM Registered DIMM. The module has all the addresses and control signals buffered to reduce capacitive loading. The module utilizes a phase lock loop to reduce the capacitive loading on the clock signals and to synchronize all SDRAM input clocks with the system clock. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVF7264R52E5667F2 memory module is 512MByte and organized as 64MX72 ECC array using (9) 64MX8 DDR2 SDRAMs in FBGA packages.

The module PCB is fabricated using the latest technology design, eight-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



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INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLT.	FEATURE	SPEED	MODE	REV
AV=AVANT	F=240-PIN DDR2 DIMM	72=X72	64 = 64M	R=REGISTERED	52 = 16Mx8x4 (DDR2 SDRAM)	E=1.8V	5 = CAS LATENCY 5	667MHZ	F=DDR2 SDRAM	REV=2

Other options may be available. Call for specific part number information on options not listed.



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