



DDR2 SDRAM UNBUFFERED DIMM MODULE, 1.8V 2GByte - 256MX72 ECC AVF7256U61E4533F2

FEATURES

JEDEC DDR2 PC2-4200 533MHz

- Clock frequency: 266MHz with CAS latency 4
- 256 byte serial EEPROM
- Data input and output masking
- Programmable burst length: 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 4
- Bi-directional Differential Data-Strobe
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- On Die Termination
- Auto refresh and self refresh capability
- Double-sided module
- 1.18 inch height

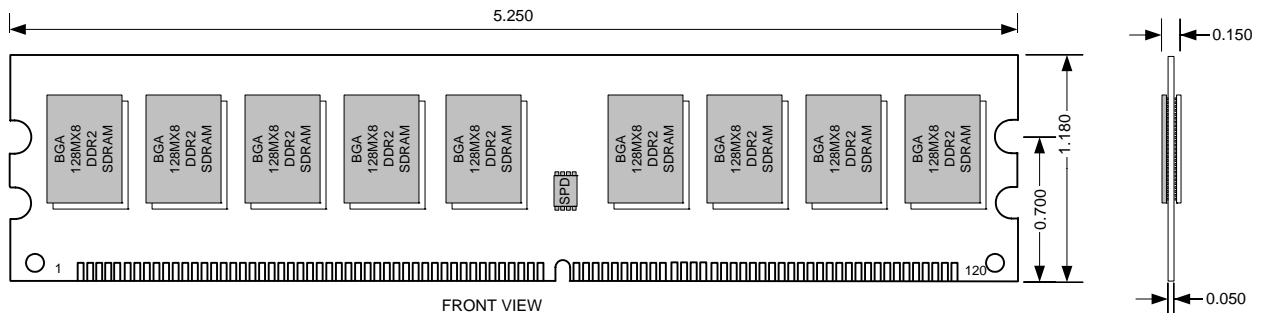
DESCRIPTION

The AVF7256U61E4533F2 is an Unbuffered DDR2 SDRAM DIMM module. This module is JEDEC Pinout compatible DDR2 SDRAM Unbuffered DIMM. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVF7256U61E4533F2 memory module is 2GByte and organized as 256MX72 array using (18) 128MX8 SDRAMs in FBGA packages.

The module PCB is fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



All gray ICs are on the front side, and all white ICs are on the back side of the module

Avant Ordering Guides

| AV | F | 72 | 56 | U | 61 | E | 4 | 533 | F | 2 |
|-----------|---------------------|-----------|-----------|----------------|---------------------------|----------|-------------------|------------|--------------|----------|
| INVENTORY | MOD. TYPE | ORG. | DENSITY | PARITY | TYPE | VOLT. | FEATURE | SPEED | MODE | REV |
| AV=AVANT | F=240-PIN DDR2 DIMM | 72=x72 | 56=256M | U = Unbuffered | 61 = 16Mx8x8 (DDR2 SDRAM) | E=1.8V | 4 = CAS LATENCY 4 | 533MHZ | F=DDR2 SDRAM | REV=2 |

Other options may be available. Call for specific part number information on options not listed.



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