



DDR2 SDRAM REGISTERED DIMM MODULE, 1.8V 2GByte - 256MX72 AVF7256R53E3400F6

FEATURES

JEDEC DDR2 PC2-3200 400MHz

- Clock frequency: 200MHz with CAS latency 3
- 256 byte serial EEPROM
- Data input and output masking
- Programmable burst length: 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 3
- Bi-directional Differential Data-Strobe
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- On Die Termination
- Auto refresh and self refresh capability
- Double-sided module
- 1.18 inch height

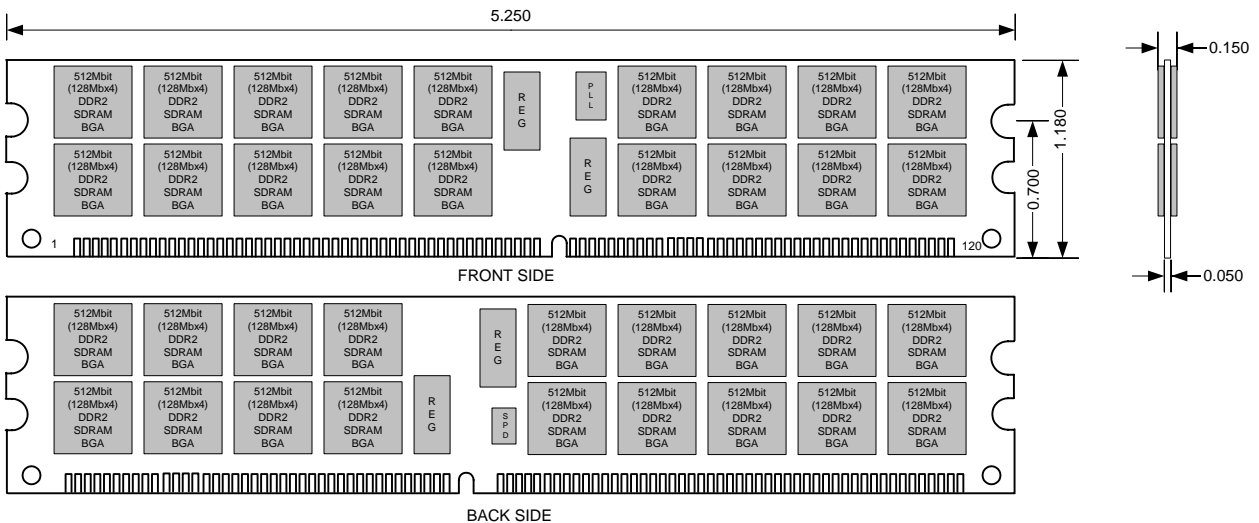
DESCRIPTION

The AVF7256R53E3400F6 is a Registered DDR2 SDRAM DIMM module. This module is JEDEC Pinout compatible DDR2 SDRAM Registered DIMM. The module has all the addresses and control signals buffered to reduce capacitive loading. The module utilizes a phase lock loop to reduce the capacitive loading on the clock signals and to synchronize all SDRAM input clocks with the system clock. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVF7256R53E3400F6 memory module is 2GByte and organized as 256MX72 ECC array using (36) 128MX4 DDR2 SDRAMs in FBGA packages.

The module PCB is fabricated using the latest technology design, eight-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



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| AV | F | 72 | 56 | R | 53 | E | 3 | 400 | F | 6 |
|-----------|-----------------------|-----------|-----------|--------------|------------------------------|----------|-------------------|------------|-----------------|----------|
| INVENTORY | MOD. TYPE | ORG. | DENSITY | PARITY | TYPE | VOLT. | FEATURE | SPEED | MODE | REV |
| AV=AVANT | F = 240-PIN DDR2 DIMM | 72=X72 | 56 = 256M | R=REGISTERED | 53 = 32Mx4x4 (DDR2 SDRAM) | E =1.8V | 3 = CAS LATENCY 3 | 400MHz | F=DDR2 SDRAM | REV=6 |

Other options may be available. Call for specific part number information on options not listed.



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