



DDR2 SDRAM REGISTERED DIMM MODULE, 1.8V 4GByte - 512MX72 AVF7251R62E5667FA-AP

FEATURES

JEDEC DDR2 PC2-5300 667MHz, Lead Free, RoHS compliant

- Clock frequency: 333MHz with CAS latency 5
- 256 byte serial EEPROM
- Data input and output masking
- Programmable burst length: 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 5
- Bi-directional Differential Data-Strobe
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- On Die Termination
- Auto refresh and self refresh capability
- Double-sided module
- 30mm (1.18 in) height

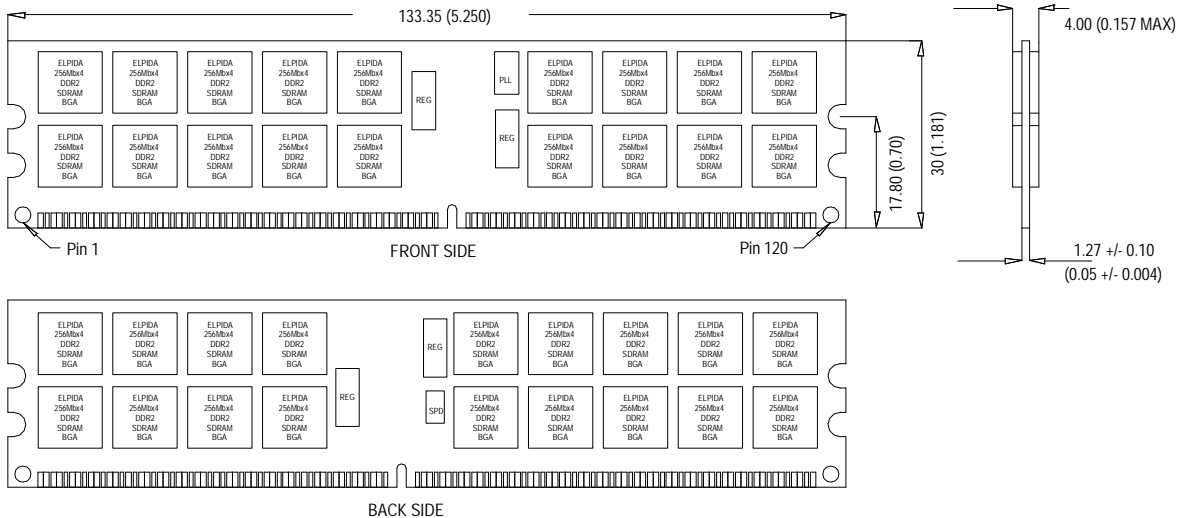
DESCRIPTION

The AVF7251R62E5667FA-AP is a Registered DDR2 SDRAM DIMM module. This module is JEDEC Pinout compatible DDR2 SDRAM Registered DIMM. The module has all the addresses and control signals buffered to reduce capacitive loading. The module utilizes a phase lock loop to reduce the capacitive loading on the clock signals and to synchronize all SDRAM input clocks with the system clock. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVF7251R62E5667FA-AP memory module is 4GByte and organized as 512MX72 ECC array using (36) 256MX4 DDR2 SDRAMs in lead free FBGA packages.

The module PCB is fabricated using the latest technology design, ten-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



NOTES: 1- All dimensions are in millimeters (inches)
2- The dimensional drawings are for reference only. Refer to the JEDEC document for additional information.

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INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLT.	FEATURE	SPEED	MODE	REV
AV=AVANT	F = 240-PIN DDR2 DIMM	72=X72	51 = 512M	R=REGISTERED	62 = 32Mx4x8 (DDR2 SDRAM)	E =1.8V	5 = CAS LATENCY 5	667MHZ	F=DDR2 SDRAM	REV=A

Other options may be available. Call for specific part number information on options not listed.



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