



DDR2 SDRAM UNBUFFERED DIMM MODULE, 1.8V 1GByte - 128MX72 ECC AVF7228U52E5800F2

FEATURES

JEDEC DDR2 PC2-6400 800MHz

- Clock frequency: 400MHz with CAS latency 5
- 256 byte serial EEPROM
- Data input and output masking
- Programmable burst length: 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 5
- Bi-directional Differential Data-Strobe
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- On Die Termination
- Auto refresh and self refresh capability
- Double-sided module
- 1.18 inch height

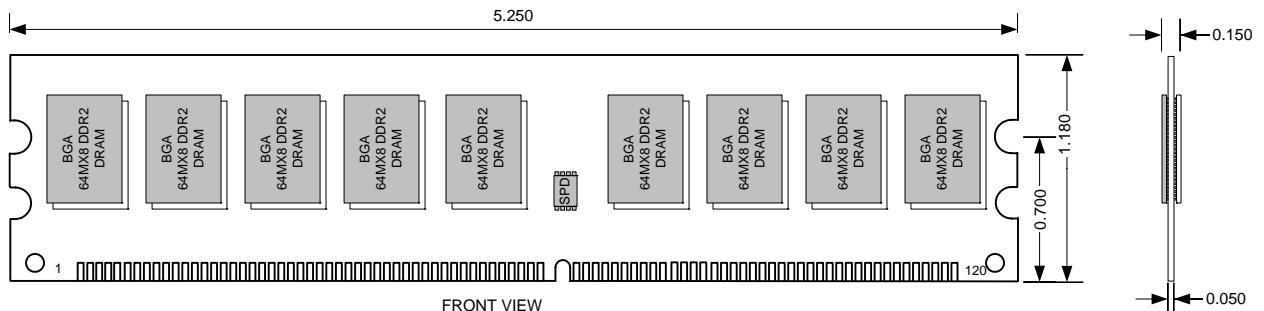
DESCRIPTION

The AVF7228U52E5800F2 is an Unbuffered DDR2 SDRAM DIMM module. This module is JEDEC Pinout compatible DDR2 SDRAM Unbuffered DIMM. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVF7228U52E5800F2 memory module is 1GByte and organized as 128MX72 array using (18) 64MX8 DDR2 SDRAMs in FBGA packages.

The module PCB is fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



All gray ICs are on the front side, and all white ICs are on the back side of the module

Avant Ordering Guides

AV	F	72	28	U	52	E	5	800	F	2
INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLT.	FEATURE	SPEED	MODE	REV
AV=AVANT	F=240-PIN DDR2 DIMM	72=x72	28 = 128M	U = Unbuffered	52 = 16Mx8x4 (DDR2 SDRAM)	E=1.8V	5 = CAS LATENCY 5	800MHZ	F=DDR2 SDRAM	REV=2

Other options may be available. Call for specific part number information on options not listed.



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