DDR SDRAM REGISTERED DIMM MODULE, 2.5V
2GByte - 256MX72
AVM7256R53C5333K7

FEATURES

JEDEC DDR 333MHz PC2700 Version 1.0

- Clock frequency: 166MHz with CAS latency 2.5
- 8K refresh per 64ms
- 256 byte serial EEPROM
- Low active and standby current consumption
- Data input and output masking
- SSTL-2 compatible inputs and outputs
- Programmable burst length: 2, 4, 8
- Decoupling capacitors at each memory device
- Programmable burst type: sequential and interleave
- Double-sided module
- Programmable CAS latency: 2.5
- 1.20 inch height
- Auto refresh and self refresh capability
- Gold card edge fingers

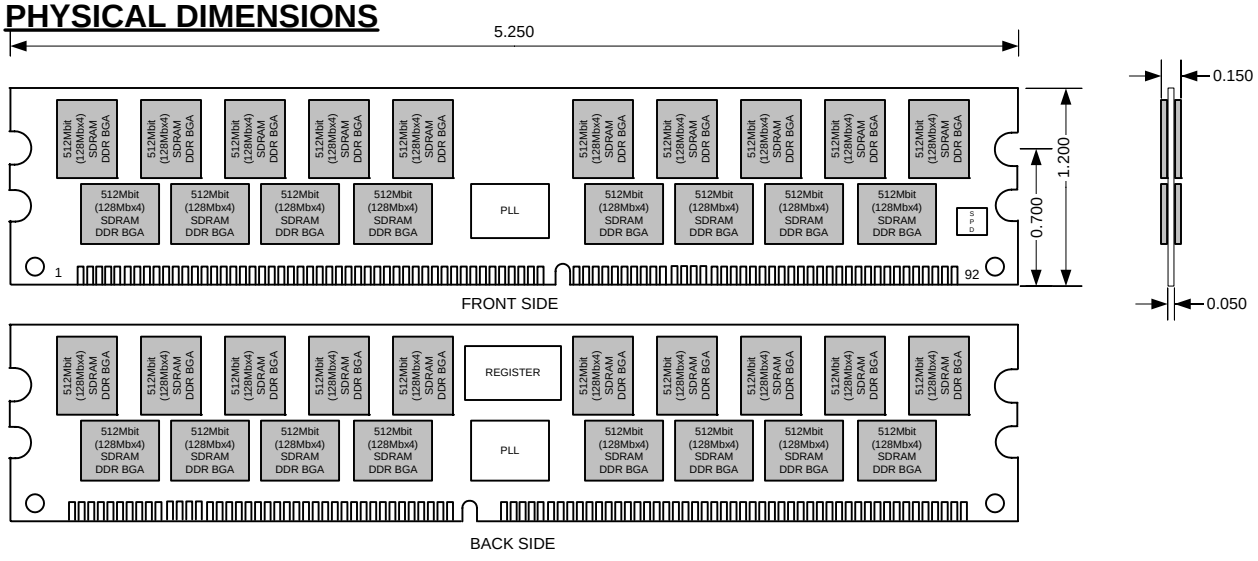
DESCRIPTION

The AVM7256R53C5333K7 is a Registered DDR SDRAM DIMM module. This module is JEDEC Pinout compatible DDR SDRAM Registered DIMM. The module has all the addresses and control signals buffered to reduce capacitive loading. The module utilizes a phase lock loop to reduce the capacitive loading on the clock signals and to synchronize all SDRAM input clocks with the system clock. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVM7256R53C5333K7 memory module is 2GByte and organized as 256MX72 ECC array using (36) 128MX4 DDR SDRAMs in BGA packages.

The module PCB is fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



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Avant Ordering Guides

AV	M	72	56	R	53	C	5	333	K	7
INVENTORY	**MOD. TYPE**	**ORG.**	**DENSITY**	**PARITY**	**TYPE**	**VOLT.**	**FEATURE**	**SPEED**	**MODE**	**REV**
AV=AVANT	M = 184-PIN DDR DIMM	72=X72	56=256M	R= REGISTERED	53 = 32Mx4x4 (8K)	C = 2.5V	5 = CAS LATENCY 2.5	333MHz	K=DDR Sdram	REV=7

Other options may be available. Call for specific part number information on options not listed.

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