DDR SDRAM SO-DIMM MODULE, 2.5V
256MByte - 32MX64
AVK6432U39C5266K2

FEATURES

JEDEC Compliant DDR 266MHz PC2100 Version 1.0

- Clock frequency: 133MHz with CAS latency 2.5
- 256 byte serial EEPROM
- Data input and output masking
- Programmable burst length: 2, 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 2.5
- Auto refresh and self refresh capability
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- SSTL-2 compatible inputs and outputs
- Decoupling capacitors at each memory device
- Double-sided module
- 1.25 inch height

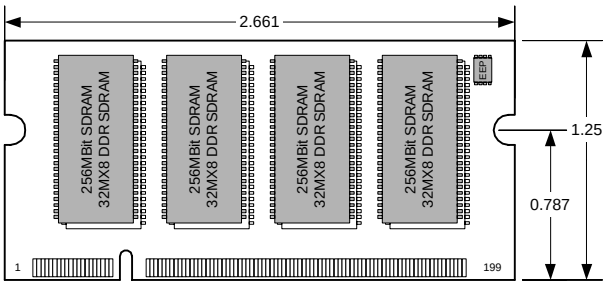
DESCRIPTION

The AVK6432U39C5266K2 family consists of Unbuffered DDR SDRAM SODIMM memory module. This module is JEDEC-standard 200-pin, small-outline, dual in-line memory module. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

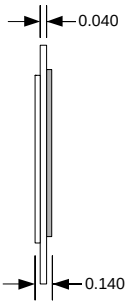
The AVK6432U39C5266K2 memory module is 256MByte and organized as a 32MX64 array using (8) 32MX8 (4 internal banks) SDRAMs in TSSOP II packages.

All memory modules are fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



All gray ICs are on the front side of the module, and all white ICs are on the back side of the module.



TSOP Package

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Avant Ordering Guides

AV	K	64	32	U	39	C	5	266	K	2
INVENTORY	**MOD. TYPE**	**ORG.**	**DENSITY**	**PARITY**	**TYPE**	**VOLT.**	**FEATURE**	**SPEED**	**MODE**	**REV**
AV = AVANT	K = 200-PIN SO-DIMM	64=X64	32=32M	U=UNBUFFERED	39=8MX8x4 (8K)	C = 2.5V	5=CAS LATENCY 2.5	266MHz	K=DDR SDRAM	REV=2

Other options may be available. Call for specific part number information on options not listed.

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