



DDR3 SDRAM UNBUFFERED DIMM MODULE, 1.5V

512MByte - 64MX64

AVF6464U52F7066G1

FEATURES

JEDEC DDR3 PC3-8500 1066MHz

- Clock frequency: 533MHz with CAS latency 7
- 256 byte serial EEPROM
- Data input and output masking
- Programmable Partial Array Self-Refresh (PASR)
- Programmable Output driver impedance control
- Programmable CAS latency: 7
- Burst lengths (BL): 8 and 4 with Burst Chop (BC)
- Bi-directional Differential Data-Strobe
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- On Die Termination (ODT)
- Auto refresh and self refresh capability
- Single-sided module
- 1.18 inch height

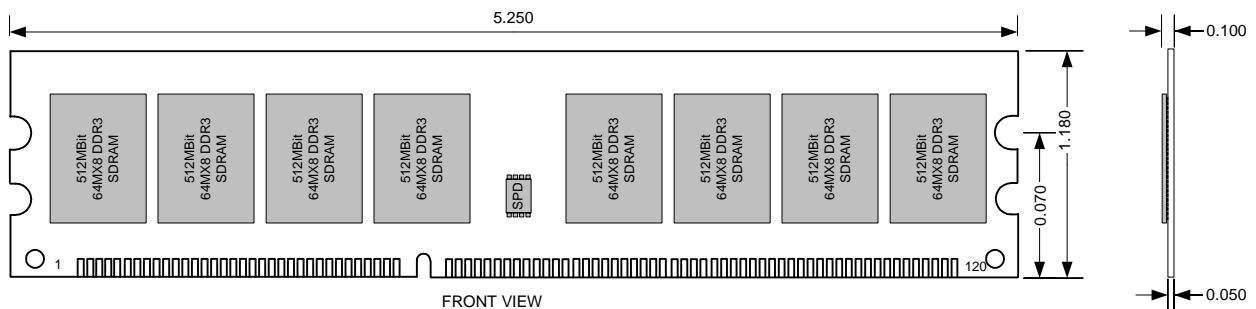
DESCRIPTION

The AVF6464U52F7066G1 is an Unbuffered DDR3 SDRAM DIMM module. This module is JEDEC Pinout compatible DDR3 SDRAM Unbuffered DIMM. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVF6464U52F7066G1 memory module is 512MByte and organized as 64MX64 array using (8) 64MX8 DDR3 SDRAMs in FBGA packages.

The module PCB is fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



Avant Ordering Guides

AV	F	64	64	U	52	F	7	066	G	1
INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLT.	FEATURE	SPEED	MODE	REV
AV=AVANT	F=240-PIN DIMM	64=x64	64 = 64M	U = Unbuffered	52 = 16Mx8x4 (DDR2 SDRAM)	F = 1.5V	7 = CAS LATENCY 7	1066MHZ	G=DDR3 SDRAM	REV=1

Other options may be available. Call for specific part number information on options not listed.



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