



DDR SDRAM DIMM MODULE, 2.5V

256MByte - 32MX64

AVM6432U40C5333K2

FEATURES

JEDEC DDR 333MHz PC2700 Version 1.0

- Clock frequency: 166MHz with CAS latency 2.5
- 256 byte serial EEPROM
- Data input and output masking
- Programmable burst length: 2, 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 2.5
- Auto refresh and self refresh capability
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- SSTL-2 compatible inputs and outputs
- Decoupling capacitors at each memory device
- Double-sided module
- 1.16 inch height

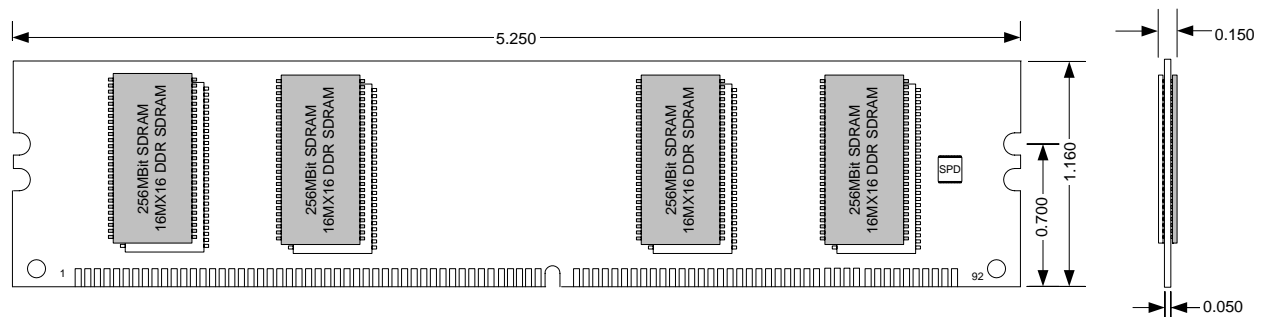
DESCRIPTION

The AVM6432U40C5333K2 is a 184 pin Unbuffered DDR SDRAM DIMM module. This module is JEDEC pinout compatible. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVM6432U40C5333K2 memory module is 256MByte and organized as 32MX64 array using (8) 16MX16 DDR SDRAMs in TSSOP II packages.

This memory module is fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



Avant Ordering Guides

AV	M	64	32	U	40	C	5	333	K	2
INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLTAGE	FEATURE	SPEED	MODE	REV
AV = AVANT	M=184-PIN DDR DIMM	64=X64	32 = 32M	U=UNBUFFERED	40=4Mx16x4 (DDR SDRAM)	C = 2.5V	5=CAS LATENCY 2.5	333MHZ	K-DDR SDRAM	REV=2

Other options may be available. Call for specific part number information on options not listed.



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