



DDR2 SDRAM REGISTERED MINI-DIMM MODULE, 1.8V

512MByte - 64MX72

AVG7264R52E5667F0-AP

FEATURES

JEDEC DDR2 PC2-5300 667MT/s, Lead Free, RoHS compliant

- Clock frequency: 333MHz with CAS latency 5
- 256 byte serial EEPROM
- Support ECC error detection and correction
- Support duplicate output strobe
- Programmable burst length: 4, 8
- Programmable CAS latency: 5
- Four-bit prefetch architecture
- Differential data strobe option
- Low active and standby current consumption
- SSTL_18 compatible inputs and outputs (1.8V)
- On-die termination (ODT)
- 8K refresh per 64ms
- Gold card edge fingers
- Double-sided module
- 18.30mm (0.72 in) height

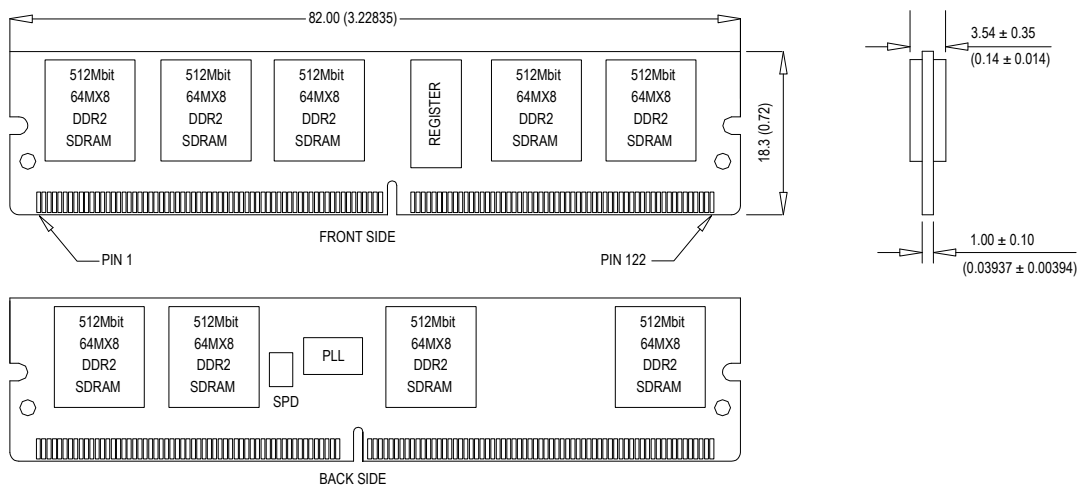
DESCRIPTION

The AVG7264R52E5667F0-AP is a Registered DDR2 SDRAM mini-DIMM module. This module is JEDEC Pinout compatible DDR2 SDRAM Registered mini-DIMM. The module has all the addresses and control signals buffered to reduce capacitive loading. The module utilizes a phase lock loop to reduce the capacitive loading on the clock signals and to synchronize all SDRAM input clocks with the system clock. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVG7264R52E5667F0-AP memory module is 512MByte and organized as 64MX72 ECC array using (9) 64MX8 DDR2 SDRAMs in lead-free FBGA packages.

The module PCB is fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



NOTES: 1- All dimensions are in millimeters (inches)
2- The dimensional drawings are for reference only. Refer to the JEDEC document for additional information.

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INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLT.	FEATURE	SPEED	MODE	REV
AV = AVANT	G = 244 PIN MINI-DIMM	72 = X72	64 = 64M	R= REGISTERED	52 = 16Mx8x4 (DDR2 SDRAM)	E = 1.8V	5 = CAS LATENCY 5	667MT/s	F = DDR2 SDRAM	DEFAULT

Other options may be available. Call for specific part number information on options not listed.



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