



DDR2 SDRAM UNBUFFERED MINI-DIMM MODULE, 1.8V 2GByte - 256MX72 ECC AVG7256U89E6800F1-AP

FEATURES

JEDEC DDR2 PC2-6400 800MT/s, Lead Free, RoHS compliant

- Clock frequency: 400MHz with CAS latency 6
- 256 byte serial EEPROM
- Support ECC error detection and correction
- Support duplicate output strobe
- Programmable burst length: 4, 8
- Programmable CAS latency: 6
- Four-bit prefetch architecture
- Differential data strobe option
- Low active and standby current consumption
- SSTL_18 compatible inputs and outputs (1.8V)
- On-die termination (ODT)
- 8K refresh per 64ms
- Gold card edge fingers
- Double-sided module
- 18.30mm (0.72 inch) height

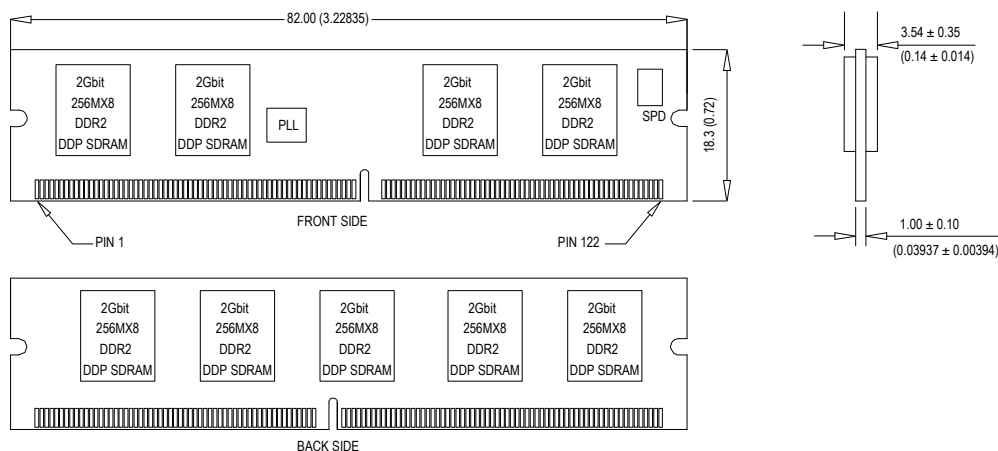
DESCRIPTION

The AVG7256U89E6800F1-AP is an Unbuffered DDR2 SDRAM miniDIMM module. This module is JEDEC Pinout compatible DDR2 SDRAM Unbuffered mini-DIMM. The module utilizes a phase lock loop to reduce the capacitive loading on the clock signals and to synchronize all SDRAM input clocks with the system clock. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVG7256U89E6800F1-AP memory module is 2GByte and organized as 256MX72 ECC array using (9) 256MX8 DDR2 DDP SDRAMs in lead-free FBGA packages.

The module PCB is fabricated using the latest technology design, ten-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



NOTES: 1- All dimensions are in millimeters (inches)
2- The dimensional drawings are for reference only. Refer to the JEDEC document for additional information.

Avant Ordering Guides

AV	G	72	56	U	89	E	6	800	F	1
INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLT.	FEATURE	SPEED	MODE	REV
AV = AVANT	G = 244 PIN DDR2 MINI-DIMM	72 = X72	56 = 256M	U=Unbuffered	89 = 64Mx8x4 (DDR2 DDP SDRAM)	E = 1.8V	6 = CAS LATENCY 6	800MT/s	F = DDR2 SDRAM	REV-1

Other options may be available. Call for specific part number information on options not listed.



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