



DDR2 SDRAM FULLY BUFFERED DIMM MODULE, 1.8V

1GByte - 128MX72

AVF7228B52E5667F0

FEATURES

JEDEC DDR2 fully buffered (FBDIMM) with ECC PC2-5300 667MHz

- Clock frequency: 333MHz with CAS latency 5
- 256 byte serial EEPROM
- SMBus interface to AMB for configuration register access
- In-band and out-of-band command access
- Automatic DDR2 SDRAM bus and channel calibration
- Programmable CAS latency: 5
- High speed, differential, point-to-point link
- Transmitter de-emphasis to reduce ISI
- MBIST and IBIST test functions
- Channel error detection and reporting
- 8K refresh per 64ms
- Gold edge contacts
- Double-sided module
- 1.18 inch height

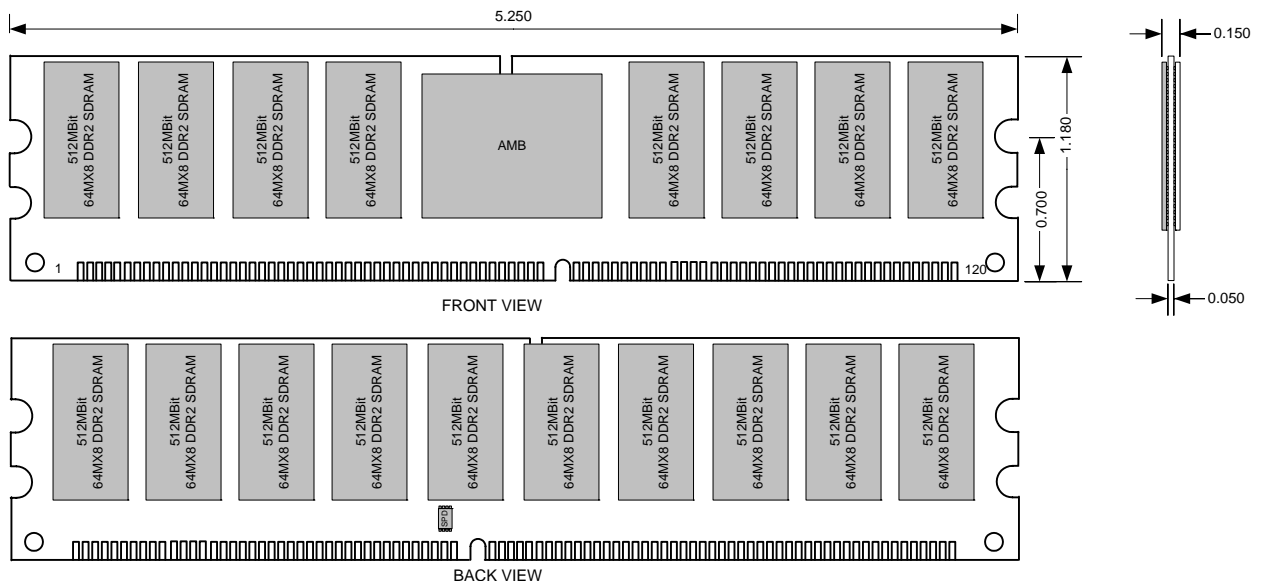
DESCRIPTION

The AVF7228B52E5667F0 is a fully buffered DDR2 SDRAM DIMM module. This module is JEDEC Pinout compatible DDR2 SDRAM Buffered DIMM with heatsink spreader. The module provides a high memory bandwidth, large capacity channel solution that has a narrow host interface. The module utilizes commodity DRAMs isolated from the channel behind a buffer on the DIMM. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVF7228B52E5667F0 memory module is 1GByte and organized as 128MX72 ECC array using (18) 64MX8 DDR2 SDRAMs in FBGA packages.

The module PCB is fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



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AV	F	72	28	B	52	E	5	667	F	0
INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLT.	FEATURE	SPEED	MODE	REV
AV=AVANT	F=240-PIN DDR2 DIMM	72=X72	28=128M	B=BUFFERED	52 = 16Mx8x4 (DDR2 SDRAM)	E=1.8V	5 = CAS LATENCY 5	667MHZ	F=DDR2 SDRAM	DEF=0

Other options may be available. Call for specific part number information on options not listed.



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