



DDR3 SDRAM REGISTERED DIMM MODULE, 1.5V 2GByte - 256MX72 AVF7256R61F9333GE-BP

FEATURES

JEDEC DDR3 PC3-10600 1333MT/s, Lead Free, RoHS compliant

- Clock frequency: 667MHz with CAS latency 9
- 256 byte serial EEPROM with Thermal Sensor
- Data input and output masking
- Asynchronous Refresh
- Programmable Output driver impedance control
- Programmable CAS latency: 9
- Burst lengths (BL): 8 and 4 with Burst Chop (BC)
- Bi-directional Differential Data-Strobe
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- On Die Termination (ODT)
- Double-sided module
- 18.75mm (0.738 inch) height

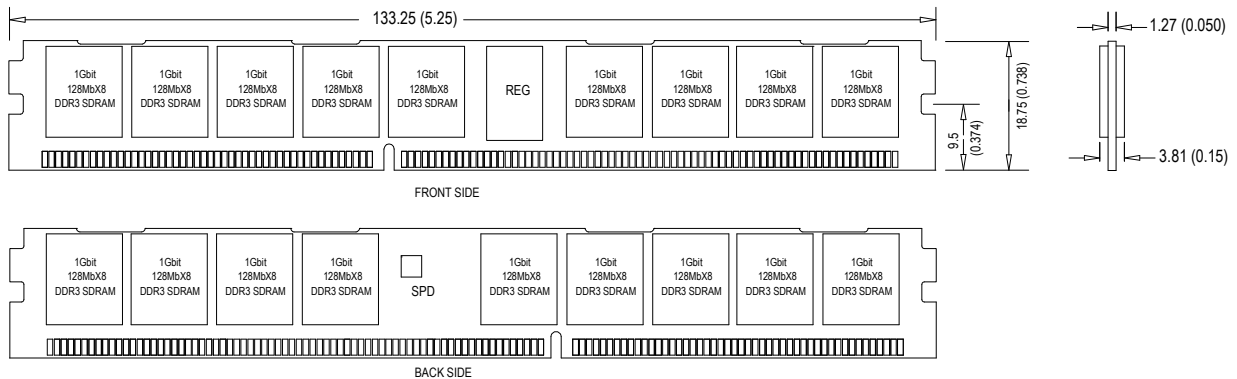
DESCRIPTION

The AVF7256R61F9333GE-BP is a VLP (Very Low Profile) Registered DDR3 SDRAM DIMM module. This module is JEDEC MO-269 R/C L DDR3 SDRAM Registered DIMM. A 256 byte Temp Sensor serial EEPROM on board can be used to store module information such as timing, configuration, density, etc. as well as to monitor the module temperature and prevent the module from exceeding the maximum operating temperature.

The AVF7256R61F9333GE-BP memory module is 2GByte and organized as 256MX72 array using (18) 128MX8 DDR3 SDRAMs in lead-free, FBGA packages.

The module PCB is fabricated using the latest technology design, eight-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



NOTES: All dimensions are in millimeters (inches) and for reference only
Refer to the JEDEC document for additional information.

Avant Ordering Guides

AV	F	72	56	R	61	F	9	333	G	E
INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLT.	FEATURE	SPEED	MODE	REV
AV=AVANT	F=240-PIN DDR3 DIMM	72=x72	56 = 256M	R = Registered	61 = 16Mx8x8 (DDR3 SDRAM)	F = 1.5V	9 = CAS LATENCY 9	1333MT/s	G=DDR3 SDRAM	REV=E

Other options may be available. Call for specific part number information on options not listed.



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