



DDR3 SDRAM REGISTERED DIMM MODULE, 1.5V 2GByte - 256MX72 AVF7256R61F9333G6-AP

FEATURES

JEDEC DDR3 PC3-10600 1333MT/s, Lead Free, RoHS compliant

- Clock frequency: 667MHz with CAS latency 9
- 256 byte serial EEPROM with Thermal Sensor
- Data input and output masking
- Asynchronous Refresh
- Programmable Output driver impedance control
- Programmable CAS latency: 9
- Burst lengths (BL): 8 and 4 with Burst Chop (BC)
- Bi-directional Differential Data-Strobe
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- On Die Termination (ODT)
- Double-sided module
- 30mm (1.181 inch) height

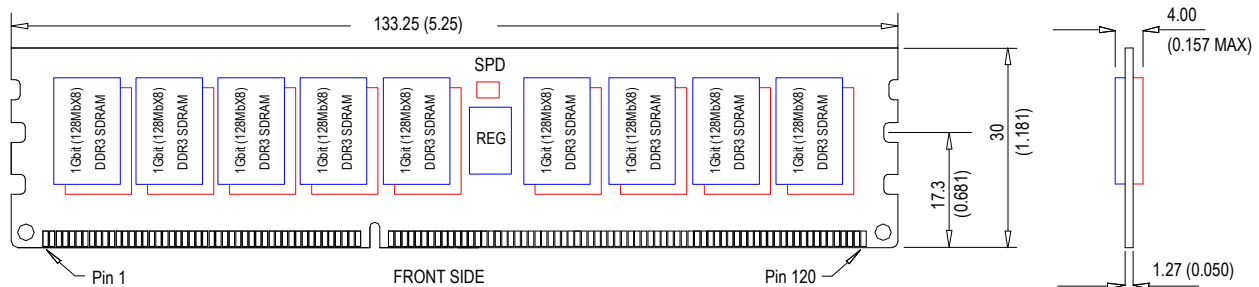
DESCRIPTION

The AVF7256R61F9333G6-AP is a Registered DDR3 SDRAM DIMM module. This module is JEDEC MO-269 R/C B DDR3 SDRAM Registered DIMM. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVF7256R61F9333G6-AP memory module is 2GByte and organized as 256MX72 array using (18) 128MX8 DDR3 SDRAMs in lead-free, FBGA packages.

The module PCB is fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



- NOTES: 1- All dimensions are in millimeters (inches)
2- The dimensional drawings are for reference only. Refer to the JEDEC document for additional information.
3- All blue ICs are on the front, and all red ICs are on the back side of the module.

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INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLT.	FEATURE	SPEED	MODE	REV
AV=AVANT	F=240-PIN DDR3 DIMM	72=x72	56=256M	R = Registered	61 = 16Mx8x8 (DDR3 SDRAM)	F = 1.5V	9 = CAS LATENCY 9	1333MT/s	G=DDR3 SDRAM	REV=6

Other options may be available. Call for specific part number information on options not listed.



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