DDR SDRAM DIMM MODULE,2.5V
1GByte - 128MX72
AVM7228U52C5266K1

FEATURES

JEDEC DDR 266MHz Version 1.0

- Clock frequency: 133MHz with CAS latency 2.5
- 256 byte serial EEPROM
- Data input and output masking
- Programmable burst length: 2, 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 2.5
- Auto refresh and self refresh capability
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- SSTL-2 compatible inputs and outputs
- Decoupling capacitors at each memory device
- Double-sided module
- 1.25 inch height

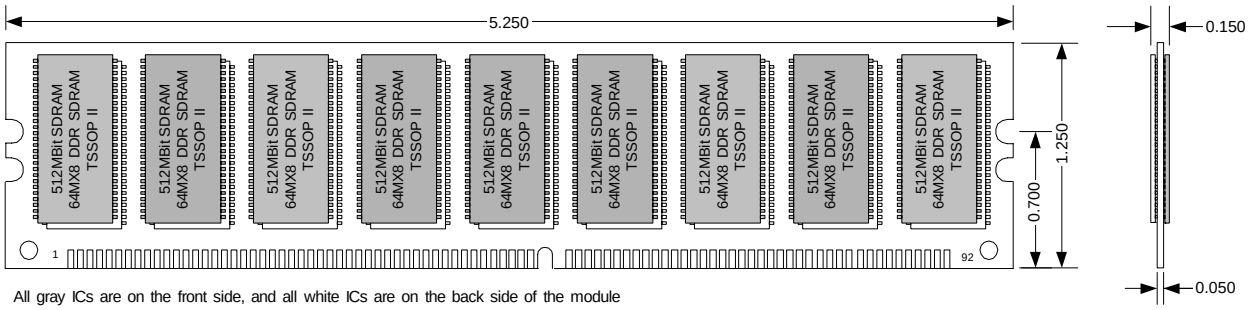
DESCRIPTION

The AVM7228U52C5266K1 is a 184 pin Unbuffered DDR SDRAM DIMM memory module. This module is JEDEC Pinout compatible. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVM7228U52C5266K1 memory module is 1GByte and organized as 128MX72 array using (18) 64MX8 DDR SDRAMs in TSSOP II packages.

This memory module is fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



All gray ICs are on the front side, and all white ICs are on the back side of the module

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Avant Ordering Guides

AV	M	72	28	U	52	C	5	266	K	1
INVENTORY	**MOD. TYPE**	**ORG.**	**DENSITY**	**PARITY**	**TYPE**	**VOLT.**	**FEATURE**	**SPEED**	**MODE**	**REV**
AV = AVANT	M=184-PIN DDR DIMM	72=X72	28=128M	U=UNBUFFERED	52=16Mx8x4 (DDR SDRAM)	C = 2.5V	5=CAS LATENCY 2.5	266MHZ	K=DDR SDRAM	REV=1

Other options may be available. Call for specific part number information on options not listed.

Avant™ Technology LP., reserves the right to change products or specifications without notice.

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