

## FEATURES

JEDEC Compliant DDR2 PC2-5300 667MHz

- Clock frequency: 333MHz with CAS latency 5
- 256 byte serial EEPROM
- Data input and output masking
- Programmable burst length: 2, 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 5
- Auto refresh and self refresh capability
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- SSTL-2 compatible inputs and outputs
- Decoupling capacitors at each memory device
- Double-sided module
- 1.18 inch height

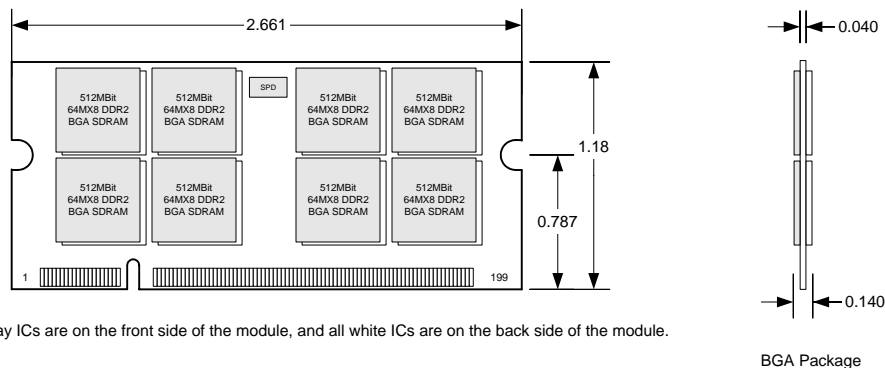
## DESCRIPTION

The AVK6428U52E5667F2 family consists of Unbuffered DDR2 SDRAM SODIMM memory module. This module is JEDEC-standard 200-pin, small-outline, dual in-line memory module. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVK6428U52E5667F2 memory module is 1GByte and organized as a 128MX64 array using (16) 64MX8 (4 internal banks) DDR2 SDRAMs in BGA packages.

All memory modules are fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

## PHYSICAL DIMENSIONS



**Avant Ordering Guides**

<b>AV</b>	<b>K</b>	<b>64</b>	<b>28</b>	<b>U</b>	<b>52</b>	<b>E</b>	<b>5</b>	<b>667</b>	<b>F</b>	<b>2</b>
INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLT.	FEATURE	SPEED	MODE	REV
Av = AVANT	K = 200-PIN SO-DIMM	64=X64	28=128M	U=UNBUFFERED	52 = 16Mx8x4	E = 1.8V	5 = CAS LATENCY 5	667MHZ	F=DDR2 SDRAM	REV=2

Other options may be available. Call for specific part number information on options not listed.



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