



DDR3 SDRAM UNBUFFERED DIMM MODULE, 1.5V 1GByte - 128MX72 ECC AVF7228U61F9333G6-BP

FEATURES

JEDEC DDR3 PC3-10600 1333MT/s, Lead Free, RoHS compliant

- Clock frequency: 667MHz with CAS latency 9
- 256 byte serial EEPROM
- Data input and output masking
- Programmable Partial Array Self-Refresh (PASR)
- Programmable Output driver impedance control
- Programmable CAS latency: 9
- Burst lengths (BL): 8 and 4 with Burst Chop (BC)
- Bi-directional Differential Data-Strobe
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- On Die Termination (ODT)
- Auto refresh and self refresh capability
- Single-sided module
- 30mm (1.181 inch) height

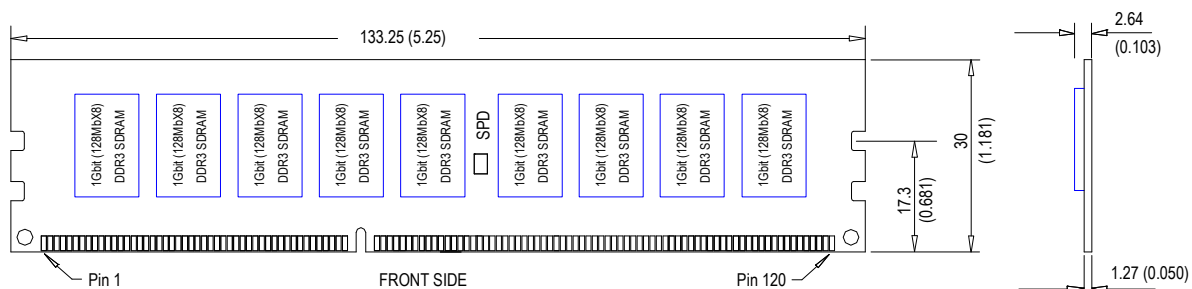
DESCRIPTION

The AVF7228U61F9333G6-BP is an Unbuffered DDR3 SDRAM DIMM module. This module is JEDEC MO-269 R/C D DDR3 SDRAM Unbuffered DIMM. A 256 byte Temp Sensor serial EEPROM on board can be used to store module information such as timing, configuration, density, etc. as well as to monitor the module temperature and prevent the module from exceeding the maximum operating temperature.

The AVF7228U61F9333G6-BP memory module is 1GByte and organized as 128MX72 array using (9) 128MX8 DDR3 SDRAMs in lead-free FBGA packages.

The module PCB is fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



NOTES: 1- All dimensions are in millimeters (inches)

2- The dimensional drawings are for reference only. Refer to the JEDEC document for additional information.

Avant Ordering Guides

AV F 72 28 U 61 F 9 333 G 6

INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLT.	FEATURE	SPEED	MODE	REV
AV=AVANT	F=240-PIN DDR3 DIMM	72=x72	28=128M	U = UNBUFFERED	61 = 16Mx8x8 (DDR3 SDRAM)	F = 1.5V	9 = CAS LATENCY 9	1333MT/s	G=DDR3 SDRAM	REV=6

Other options may be available. Call for specific part number information on options not listed.



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